

IN THE SPECIFICATION:

Please amend page 8, lines 6-11, as follows:

Fig. 9 is a state transition diagram showing the state of the address counter 41 and the state of the timing-signal generation circuit 50 when the synchronization control circuit 40 carries out a synchronization control process according to a synchronization control routine shown in ~~[[Fig. 7]]~~ Fig. 8;

Please delete in its entirety page 59, lines 2-23, and substitute therefore the following:

An information recording/reproducing apparatus and information reproducing method records information data, regardless of an external disturbance. According to a demodulated timing pulse, a synchronization signal and address data representative of an address is demodulated from a readout signal. An error correction process carried out on the address data obtains corrected address data. A determination is made if the corrected address data is a correct address. When the synchronization signal has a period equal to a predetermined period, a synchronization process is executed to place the demodulated timing pulse in synchronism with the synchronization signal, wherein, when the corrected address data is determined as a correct address, then the re-synchronization process is put into stand-by until the corrected address data is determined as an incorrect address. When the synchronization signal is temporarily influenced by an external disturbance, information data recording is completed without a re-synchronization process and interrupting recording operation.